

## NADAR SARSWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

<b>Course/Branch :</b> B.E./CSE	<b>Year / Semester :</b> II/III	Format No.	NAC/TLP-07a.13
<b>Subject Code :</b> CS8351	<b>Subject Name :</b> Digital Principles and System Design	Rev. No.	02
<b>Unit No :</b> 2	<b>Unit Name :</b> Combinational logic	Date	30.09.2020

### OBJECTIVE TYPE QUESTION BANK

S. No.	Objective Questions (MCQ /True or False / Fill up with Choices )	BTL
1	How many 3-line-to-8-line decoders are required for a 1-of-32 decoder? A) 1 B) 2 C) <b>4</b> D) 8	L2
2	Which of the following combinations of logic gates can decode binary 1101? a) One 4-input AND gate b) <b>One 4-input AND gate, one inverter</b> c) One 4-input AND gate, one OR gate d) One 4-input NAND gate, one inverter	L2
3	The carry propagation can be expressed as _____ a) $C_p = AB$ b) <b><math>C_p = A + B</math></b> c) All but Y0 are LOW d) All but Y0 are HIGH	L4
4	$2^9$ input circuit will have total of _____ a) 32 entries b) 128 entries c) 256 entries d) <b>512 entries</b>	L5
5	The output sum of two decimal digits can be represented in _____ a) Gray Code b) Excess-3 c) <b>BCD</b> d) Hexadecimal	L1
6	The addition of two decimal digits in BCD can be done through _____ a) <b>BCD adder</b> b) Full adder c) Ripple carry adder d) Carry look ahead adder	L2
7	The simplified expression of full adder carry is _____ a) <b><math>c = xy+xz+yz</math></b> b) $c = xy+xz$ c) $c = xy+yz$ d) $c = x+y+z$	L2
8	Decimal digit in BCD can be represented by _____ a) 1 input line b) 2 input lines c) 3 input lines d) <b>4 input lines</b>	L4
9	A serial subtractor can be obtained by converting the serial adder by using the a) 1's complement system	L5

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	<p>b) <b>2's complement system</b></p> <p>c) 10's complement</p> <p>d) 9's complement</p>	
10	<p>Why is parallel data transmission preferred over serial data transmission for most applications?</p> <p>a) It is much slower</p> <p>b) It is cheaper</p> <p>c) More people use it</p> <p>d) <b>It is much faster</b></p>	L1
11	<p>The carry look ahead adder is based on the principle of looking at the lower order bits of _____ and _____ if a high order carry is generated.</p> <p>a) Addend, minuend</p> <p>b) Minuend, subtrahend</p> <p>c) Addend, minuend</p> <p>d) <b>Augend, addend</b></p>	L2
12	<p>In serial addition, the addition is carried out _____</p> <p>a) 3 bit per second</p> <p>b) Byte by byte</p> <p>c) <b>Bit by bit</b></p> <p>d) All bits at the same time</p>	L2
13	<p>A D flip-flop is used in a 4-bit serial adder, why?</p> <p>a) It is used to invert the input of the full adder</p> <p>b) It is used to store the output of the full adder</p> <p>c) <b>It is used to store the carry output of the full adder</b></p> <p>d) It is used to store the sum output of the full adder</p>	L4
14	<p>What is ripple carry adder?</p> <p>a) <b>The carry output of the lower order stage is connected to the carry input of the next higher order stage</b></p> <p>b) The carry input of the lower order stage is connected to the carry output of the next higher order stage</p> <p>c) The carry output of the higher order stage is connected to the carry input of the next lower order stage</p> <p>d) The carry input of the higher order stage is connected to the carry output of the lower order stage</p>	L5
15	<p>Carry lookahead logic uses the concepts of _____</p> <p>a) Inverting the inputs</p> <p>b) Complementing the outputs</p> <p>c) <b>Generating and propagating carries</b></p> <p>d) Ripple factor</p>	L1
16	<p>One that is not the outcome of magnitude comparator is _____</p> <p>a) <math>a &gt; b</math></p> <p>b) <math>a - b</math></p>	

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	c) $a < b$ d) $a = b$	L2
17	In a comparator, if we get input as $A > B$ then the output will be _____ a) <b>1</b> b) 0 c) A d) B	L2
18	A circuit that compares two numbers and determine their magnitude is called a) Height comparator b) Size comparator c) Comparator d) <b>Magnitude comparator</b>	L4
19	TTL 74LS85 is a _____ a) 1-bit digital comparator b) <b>4-bit magnitude comparator</b> c) 8-bit magnitude comparator d) 8-bit word comparator	L5
20	A code converter is a logic circuit that _____ a) Inverts the given input b) Converts into decimal number c) <b>Converts data of one type into another type</b> d) Converts to octal	L1
21	Convert binary number into gray code: 100101. a) 101101 b) 001110 c) <b>110111</b> d) 111001	L2
22	How many inputs will a decimal-to-BCD encoder have? a) 4 b) 8 c) <b>10</b> d) 16	L2
23	If we record any music in any recorder, such types of process is called _____ a) Multiplexing b) <b>Encoding</b> c) Decoding d) Demultiplexing	L4
24	Can an encoder be called as multiplexer? a) No b) <b>Yes</b> c) Sometimes d) Never	L5
25	The word demultiplex means _____	L1

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	a) One into many b) Many into one c) Distributor d) <b>One into many as well as Distributor</b>	
26	Why is a demultiplexer called a data distributor? a) <b>The input will be distributed to one of the outputs</b> b) One of the inputs will be selected for the output c) The output will be distributed to one of the inputs d) Single input to Single Output	L2
27.	Total number of inputs in a half adder is _____ a) <b>2</b> b) 3 c) 4 d) 1	L2
28.	In which operation carry is obtained? a) Subtraction b) <b>Addition</b> c) Multiplication d) Both addition and subtraction	L4
29.	If A and B are the inputs of a half adder, the sum is given by _____ a) A AND B b) A OR B c) <b>A XOR B</b> d) A EX-NOR B	L5
30.	Half-adders have a major limitation in that they cannot _____ a) Accept a carry bit from a present stage b) Accept a carry bit from a next stage c) <b>Accept a carry bit from a previous stage</b> d) Accept a carry bit from the following stages	L1
31.	The difference between half adder and full adder is _____ a) Half adder has two inputs while full adder has four inputs b) Half adder has one output while full adder has two outputs c) <b>Half adder has two inputs while full adder has three inputs</b> d) All of the Mentioned	L2
32.	A logic circuit that provides a HIGH output for both inputs HIGH or both inputs LOW is a) <b>Ex-NOR gate</b> b) OR gate c) Ex-OR gate d) NAND gate	L2
33.	Which digital system translates coded characters into a more useful form? a) Encoder b) Display c) Counter	L4

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	<b>d) Decoder</b>	
34.	What control signals may be necessary to operate a 1-line-to-16 line decoder? a) Flasher circuit control signal b) <b>A LOW on all gate enable inputs</b> c) Input from a hexadecimal counter d) A HIGH on all gate enable circuits	L5
35.	How many inputs are required for a 1-of-16 decoder? a) 2 b) 16 c) 8 d) <b>4</b>	L1
36.	The full form of LCD is _____ a) <b>Liquid Crystal Display</b> b) Liquid Crystalline Display c) Logical Crystal Display d) Logical Crystalline Display	L2
37.	The optical properties of liquid crystals depend on the direction of _____ a) Air b) Solid c) <b>Light</b> d) Water	L2
38.	By which properties, the orientation of molecules in a layer of liquid crystals can be changed? a) Magnetic field b) <b>Electric field</b> c) Electromagnetic field d) Gallois field	L4
39.	LCDs operate from a voltage ranges from _____ a) <b>3 to 15V</b> b) 10 to 15V c) 10V d) 5V	L5
40.	In 7 segment display, how many LEDs are used? a) 8 b) <b>7</b> c) 10 d) 9	L1