NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : B.E/ECE	Year / Semester : IV/ VII	Format No.	NAC/TLP-07a.13
Subject Code : EC8791	Subject Name : Embedded and Real Time Systems	Rev. No.	02
Unit No : 1	Unit Name : Introduction to Embedded system Design	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

S. No.	Objective Questions (MCQ /True or False / Fill up with Choices)	BTL
1	Which of the following allows the reuse of the software and the hardware components? **a) platform based design** b) memory design c) peripheral design d) input design	L2
2	Which of the following is the design in which both the hardware and software are considered during the design? a) platform based design b) memory based design **c) software/hardware codesign** d) peripheral design	L2
3	What does API stand for? a) address programming interface **b) application programming interface** c) accessing peripheral through interface d) address programming interface	L4
4	Which activity is concerned with identifying the task at the final embedded systems? a) high-level transformation b) compilation c) scheduling **d) task-level concurrency management**	L5
5	Which of the following is a process of analyzing the set of possible designs? **a) design space exploration** b) scheduling c) compilation d) hardware/software partitioning	L1
6	Which of the following is a meet-in-the-middle approach? a) peripheral based design **b) platform based design** c) memory based design d) processor design	L1

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7	**Embedded systems applications typically involve processing information as _____** **A. Distance** B. Signals C. Block level D. Logical volumes	L2
8	Which microcontrollers are adopted for designing medium scale embedded systems? a. 8-bit **b. 16-bit to 32-bit** c. 64-bit d. All of the above	L1
9	How are the instructions executed in DSP Processors? **a. In Parallel manner** b. In Sequential manner c. Both a and b d. None of the above	L1
10	Which status flag in x86 family is used to enable or disable the interrupt especially when the Pentium processor operates in the virtual mode? a. ID b. VIP **c. VIF** d. AC	L3
11	Which control register in x86 family is reserved for future use and generally not adopted for current implementation? a. CR0 **b. CR1** c. CR2 d. CR4	L2
12	Which type of non-privileged processor mode is entered due to raising of high priority of an interrupt? a. User mode **b. Fast Interrupt Mode (FIQ)** c. Interrupt Mode (IRQ) d. Supervisor Mode (SVC)	L3
13	At an active HIGH reset pin of 8051 microcontroller, for how many machine cycles should the positive going pulse be provided, if the power is switched ON? a. only one	L1

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	b. two c. three d. four	
14	Which is the first microcontroller? a) 8051 b) Arm **c) TMS1000** d) Intel 4004	L1
15	What does CCR stand for? **a) Condition code register** b) Computing code register c) Complex code register d) Code control register	L2
16	In the below diagram, identify the register of MC68HC05 **a) CCR** b) PC c) SP d) IV	L3
17	Which of the following can be a paired set of 16-bit register? a) CD **b) HL** c) AB d) EH	L1
18	Which signal is used to differentiates the access from a normal memory cycle? a) HALT b) RESET c) MREQ **d) IORQ**	L5
19	What is CISC? a) Computing instruction set complex	L1

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	b) Complex instruction set computing c) Complimentary instruction set computing d) Complex instruction set complementary	
20	It retains its content when power is removed. What type of memory is this? a) Volatile memory **b) Nonvolatile memory** c) RAM d) SRAM	L1
21	Name a volatile memory. **a) RAM** b) EPROM c) ROM d) EEPROM	L2
22	How an embedded system communicate with the outside world? **a) Peripherals** b) Memory c) Input d) Output	L1
23	Which of the following are external pins whose logic state can be controlled by the processor to either be a logic zero or logic one is known as a) Analogue value b) Display values **c) Binary values** d) Time derived digital outputs	L1
24	Which are the 4 general purposes 16 bit register in Intel 80286? a) CS,DS,SS,ES **b) AX,BX,CX,DX** c) IP,FL,DI,SI d) DI,SI,BP,SP	L3
25	How is expanded memory accessed in 80286? **a) Paging** b) Interleaving c) RAM	L2

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	d) External storage	
26	Which are the two register available in the protected mode of 80286? a) General and segmented b) General and pointer **c) Index and base pointer** d) Index and segmented	L2
27	What kind of support does 80286 access in protected mode? a) Real mode b) Address access c) Data access **d) Virtual memory**	L5
28	Which of the following processor possess memory management? a) 8086 b) 8088 **c) 80286** d) 8051	L2
29	Which is the interrupt vector that functions as invalid opcode? a) 9 b) 8 c) 7 **d) 6**	L1
30	Which of the following possess the same set of instructions? a) 8088 and 80286 **b) 8086 and 80286** c) 8051 and 8088 d) 8051 and 8086	L1