

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : B.E/ECE	Year / Semester : IV/ VII	Format No.	NAC/TLP-07a.13
Subject Code : EC8791	Subject Name : Embedded and Real Time Systems	Rev. No.	02
Unit No : 3	Unit Name : Embedded Programming	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

S. No.	Objective Questions (MCQ /True or False / Fill up with Choices)	BTL
1	Which of the following function can interpret data in the C language? a) printf b) scanf c) proc d) file	L2
2	What is the first stage of the compilation process? a) pre-processing b) post-processing c) compilation d) linking	L2
3	Which of the following produces an assembler file in the compilation process? a) pre-processor b) assembler c) compiler d) post-processing	L4
4	Which file is converted to an object file? a) hex file b) decoded file c) coded file d) assembly file	L5
5	Which of the following contains the hexadecimal coding? a) object file b) assembly file c) coded file d) decoded file	L1
6	Which of the following processes the source code before it goes to the compiler? a) compiler b) simulator c) pre-processor d) emulator	L1
7	Which of the following allows the programmer to define constants?	L2

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : B.E/ECE	Year / Semester : IV/ VII	Format No.	NAC/TLP-07a.13
Subject Code : EC8791	Subject Name : Embedded and Real Time Systems	Rev. No.	02
Unit No : 3	Unit Name : Embedded Programming	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

		<p>a) pre-processor b) compiler c) emulator d) debugger</p>	
8		<p>Which statement replaces all occurrences of the identifier with string?</p> <p>a) # define identifier string b) # include c) # define MACRO() d) # ifdef</p>	L1
9		<p>Which of the following has the include file?</p> <p>a) emulator b) debugger c) pre-processor d) simulator</p>	L1
10		<p>Which statement is used to condense the code to improve the eligibility?</p> <p>a) # define MACRO() b) # include c) if d) else-if</p>	L3
11		<p>Which programming algorithm is used in the starting process of the FRIDGE?</p> <p>a) C++ b) JAVA c) C d) BASIC</p>	L2
12		<p>high loop transformations have several instances of the loop body?</p> <p>a) loop fusion b) loop unrolling c) loop fission d) loop tiling</p>	L3
13		<p>What does ICE stand for?</p> <p>a) in-circuit emulation b) in-code EPROM c) in-circuit EPOM d) in-code emulation</p>	L1

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : B.E/ECE	Year / Semester : IV/ VII	Format No.	NAC/TLP-07a.13
Subject Code : EC8791	Subject Name : Embedded and Real Time Systems	Rev. No.	02
Unit No : 3	Unit Name : Embedded Programming	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

14	<p>The addressing mode where the EA of the operand is the contents of Rn is _____</p> <p>a) Pre-indexed mode b) Pre-indexed with write back mode c) Post-indexed mode d) None of the mentioned</p>	L1
15	<p>Which of the following does not have the ability to get hundred individual signal cables into the probe in the emulation technique?</p> <p>a) OnCE b) BDM c) ICE d) JTAG</p>	L2
16	<p>What does JTAG stand for?</p> <p>a) joint tag address group b) joint test address group c) joint test access group d) joint test action group</p>	L3
17	<p>Which of the following allows access to all the hardware within the system?</p> <p>a) debugger b) JTAG c) onboard debugger d) simulator</p>	L1
18	<p>Which facility provides the provision of the debug ports in the ICE technique?</p> <p>a) simulator b) emulator c) debug support d) jtag</p>	L5
19	<p>Which of the following takes the processor, when the processor enters the BDM mode?</p> <p>a) address code b) high-level microcode c) low-level microcode d) data code</p>	L1
20	<p>Which is the standard C compiler used for the UNIX systems?</p> <p>a) simulator</p>	L1

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : B.E/ECE	Year / Semester : IV/ VII	Format No.	NAC/TLP-07a.13
Subject Code : EC8791	Subject Name : Embedded and Real Time Systems	Rev. No.	02
Unit No : 3	Unit Name : Embedded Programming	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

	b) compiler c) cc d) sc		
21	a) transistor b) darlington transistor pair c) resistor d) capacitor	Which of the following can be used for providing high gain?	L2
22	a) current b) voltage c) resistance d) conductance	Which of the following determines the brightness of LEDs?	L1
23	a) test pattern b) debugger pattern c) bit pattern d) byte pattern	Which of the following is a set of specially selected input patterns?	L1
24	a) fault coverage b) test pattern c) size of the test pattern d) number of errors	How is the quality of the test pattern evaluated?	L3
25	a) 4800bits/sec b) 1200bits/sec c) 12000bit/sec d) 9600bits/sec	What is the speed of the 8250 UART?	L2
26	a) test pattern b) JTAG c) FSM d) CRC	Which of the following is also known as boundary scan?	L2

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : B.E/ECE	Year / Semester : IV/ VII	Format No.	NAC/TLP-07a.13
Subject Code : EC8791	Subject Name : Embedded and Real Time Systems	Rev. No.	02
Unit No : 3	Unit Name : Embedded Programming	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

27	What is FSM? a) Fourier state machine b) finite state machine c) fast state machine d) free state machine	L5
28	Which compiler is based on the precise measurements of two fixed configurations? a) first power model b) second power model c) third power model d) fourth power model	L2
29	How can one compute the power consumption of the cache? a) Lee power model b) First power model c) Third power model d) CACTI	L1
30	What is BDD? a) boolean decision diagram b) binary decision diagrams c) binary decision device d) binary device diagram	L1
31	Which of the following is possible to locate errors in the specification of the future bus protocol? a) EMC b) HOL c) BDD d) FOL	L2
32	What is CTL? a) computational tree logic b) code tree logic c) cpu tree logic d) computer tree logic	L3