

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : BE / EEE	Year / Semester : III / V	Format No.	NAC/TLP-07a.13
Subject Code :EE8551	Subject Name :Microprocessors and Microcontrollers	Rev. No.	02
Unit No : 01	Unit Name : 8085 Processor	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

S. No.	Objective Questions (MCQ /True or False / Fill up with Choices)	BTL
1	8085 microprocessor has how many pins A) 30 B) 39 C) 40 D) 41	L1
2	Which one of the following is not a vectored interrupt? A) TRAP B) INTR C) RST 7.5 D) RST 3	L2
3	In 8085 microprocessor, the RST6 instruction transfer programme execution to following location A) 0030H B) 0024H C) 0048H D) 0060H	L3
4	HLT opcode means A) Load data to accumulator B) Store in memory C) Load accumulator with contents of register D) End of Program	L2
5	In 8085 names of the 16 bit registers are A) Stack pointer B) Program counter C) Both A and B D) None of these	L1
6	What is SIM? A) Select interrupt B) Sorting interrupt mask	L2

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	<p>C) Set interrupt mask D) None of these</p>	
7	<p>A field programmable ROM is called</p> <p>A) MROM B) PROM C) FROM D) FPROM</p>	L1
8	<p>The number of output pins in 8085 microprocessors are</p> <p>A) 27 B) 40 C) 21 D) 19</p>	L1
9	<p>The program counter in a 8085 micro-processor is a 16-bit register, because</p> <p>A) It counts 16 bit at a time B) There are 16 address lines C) It facilitates the user storing 16 bit data temporarily D) It has to fetch two 8 bit data at a time</p>	L2
10	<p>Output of the assembler in machine codes is referred to as</p> <p>A) Object program B) Source program C) Macro instruction D) Symbolic addressing</p>	L2
11	<p>Which of the following statements for intel 8085 is correct?</p> <p>A) Program counter(PC) specifies the address of the instruction last executed B) PC specifies the address of the instruction being executed C) PC specifies the address of the instruction to be executed D) PC specifies the number of instructions executed so far</p>	L1
12	<p>Which one of the following is not correct?</p> <p>A) Bus is a group of wires</p>	L1

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	<p>B) Bootstrap is a technique or device for loading first instruction C) An instruction is a set of bits that defines a computer operation D) An interrupt signal is required at the start of every program</p>	
13	<p>The processor status word of 8085 microprocessor has five flags namely:</p> <p>A) S, Z, AC, P, CY B) S, OV, AC, P, CY C) S, Z, OV, P, CY D) S, Z, AC, P, OV</p>	L1
14	<p>The cycle required to fetch and execute an instruction in a 8085 microprocessor is which one of the following?</p> <p>A) Clock cycle B) Memory cycle C) Machine cycle D) Instruction cycle</p>	L2
15	<p>Which is the microprocessor comprises:</p> <p>A) Register section B) One or more ALU C) Control unit D) All of these</p>	L1
16	<p>What is the store by register?</p> <p>A) Data B) Operands C) Memory D) None of these</p>	L1
17	<p>Accumulator based microprocessor example are:</p> <p>A) Intel 8085 B) Motorola 6809 C) A and B D) None of these</p>	L2
18	<p>A set of register which contain are:</p> <p>A) data</p>	L1

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	<p>B) Memory addresses C) Result D) All of these</p>	
19	<p>There are primarily two types of register:</p> <p>A. General purpose register B. Dedicated register C. A and B D. None of these</p>	L3
20	<p>BCD stands for:</p> <p>A) Binary coded decimal B) Binary coded decoded C) Both a & b D) none of these</p>	L2
21	<p>Which is used to store critical pieces of data during subroutines and interrupts:</p> <p>A) Stack B) Queue C) Accumulator D) Data register</p>	L1
22	<p>The data in the stack is called:</p> <p>A) Pushing data B) Pushed C) Pulling D) None of these</p>	L3
23	<p>Which is not the control bus signal:</p> <p>A) READ B) WRITE C) RESET D) None of these</p>	L1
24	<p>PROM stands for:</p> <p>A) Programmable read-only memory B) Programmable read write memory</p>	L2

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	<p>C) Programmer read and write memory D) None of these</p>	
25	<p>Each memory location has:</p> <p>A) Address B) Contents C) Both A and B D) None of these</p>	L1
26	<p>The RAM which is created using bipolar transistors is called:</p> <p>A) Dynamic RAM B) Static RAM C) Permanent RAM D) DDR RAM</p>	L2
27	<p>EPROM stands for:</p> <p>A) Erasable Programmable read-only memory B) Electrically Programmable read write memory C) Electrically Programmable read-only memory D) None of these</p>	L3
28	<p>The CPU sends out a ____ signal to indicate that valid data is available on the data bus:</p> <p>A) Read B) Write C) Both A and B D) None of these</p>	L1
29	<p>BIU STAND FOR:</p> <p>A) Bus interface unit B) Bess interface unit C) A and B D) None of these</p>	L3
30	<p>The four index register can be used for:</p> <p>A) Arithmetic operation B) Multipulation operation C) Subtraction operation</p>	L1

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	D) All of these	
31	<p>The CPU removes the ___ signal to complete the memory write operation:</p> <p>A) Read B) Write C) Both A and B D) None of these</p>	L2
32	<p>EU STAND FOR:</p> <p>A) Execution unit B) Execute unit C) Exchange unit D) None of these</p>	L1
33	<p>Eight of the register are known as:</p> <p>A) General- purpose register B) Pointer or index registers C) Segment registers D) Other register</p>	L2
34	<p>CS Stand for:</p> <p>A) Code segment B) Coot segment C) Cost segment D) Counter segment</p>	L3
35	<p>In 8085 microprocessor system with memory mapped I/O, which of the following is true?</p> <p>A) Devices have 8-bit address line B) Devices are accessed using IN and OUT instructions C) There can be maximum of 256 input devices and 256 output devices D) Arithmetic and logic operations can be directly performed with the I/O data</p>	L1
36	<p>Consider the following statements: In 8085 microprocessor, data-bus and address bus are multiplexed in order to Which of these statements is/are correct?</p> <p>A) Increase the speed of microprocessor. B) Reduce the number of pins.</p>	L1

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	C) Connect more peripheral chips.	
37	<p>In intel 8085A microprocessor ALE signal is made high to</p> <p>A) Enable the data bus to be used as low order address bus B) To latch data D0-D7 from data bus C) To disable data bus D) To achieve all the functions listed above</p>	L1
38	<p>DS Stand for:</p> <p>A) Data segment B) Direct segment C) Declare segment D) Divide segment</p>	L1
39	<p>8085 microprocessor is an 8-bit microprocessor designed by?</p> <p>A) IBM B) Dell C) Intel D) VAX</p>	L2
40	<p>Flag register is an 8-bit register having _____ 1-bit flip-flops.</p> <p>A) 3 B) 4 C) 5 D) 6.</p>	L1
41	<p>What is true about Program counter?</p> <p>A) It is an 8-bit register, which holds the temporary data of arithmetic and logical operations. B) When an instruction is fetched from memory then it is stored in the program counter C) It provides timing and control signal to the microprocessor D) It is a 16-bit register used to store the memory address location of the next instruction to be executed.</p>	L2