

NADAR SARASWATHI COLLEGE OF ENGINEERING AND TECHNOLOGY, THENI.

Course/Branch : BE / EEE	Year / Semester : III / V	Format No.	NAC/TLP-07a.13
Subject Code :EE8551	Subject Name :Microprocessors and Microcontrollers	Rev. No.	02
Unit No : 03	Unit Name : 8051 Micro Controller	Date	30.09.2020

OBJECTIVE TYPE QUESTION BANK

S. No.	Objective Questions (MCQ /True or False / Fill up with Choices)	BTL
1	Which of the following is not an addressing mode of 8051? A) register instructions B) register specific instructions C) indexed addressing D) None	L1
2	The symbol, 'address 16' represents the 16-bit address which is used by the instructions to specify the A) destination address of CALL B) source address of JUMP C) destination address of call or jump D) source address of call or jump	L2
3	The storage of addresses that can be directly accessed is A) external data RAM B) internal data ROM C) internal data RAM and SFRS D) external data ROM and SFRS	L1
4	The address register for storing the 16-bit addresses can only be A) stack pointer B) data pointer C) instruction register D) Accumulator	L2
5	The address register for storing the 8-bit addresses can be A) R0 of the selected bank of register B) R1 of the selected bank of register C) Stack pointer D) All of the mentioned	L1
6	The instruction, ADD A, #100 performs A) 100(decimal) is added to contents of address register B) 100(decimal) is subtracted from the accumulator C) 100(decimal) is added to contents of an accumulator	L3

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	D) None	
7	In which of these addressing modes, a constant is specified in the instruction, after the opcode byte? A) register instructions B) Register specific instructions C) Direct addressing D) immediate mode	L1
8	The instruction, ADD A, R7 is an example of A) register instructions B) Register specific instructions C) Indexed addressing D) none	L1
9	The only memory which can be accessed using indexed addressing mode is A) RAM B) ROM C) Main memory D) Program memory	L2
10	The data address of look-up table is found by adding the contents of A) accumulator with that of program counter B) accumulator with that of program counter or data pointer C) data register with that of program counter or accumulator D) data register with that of program counter or data pointer	L1
11	8051 Microcontrollers are manufactured by which of the following companies? A) Atmel B) Philips C) Intel D) All of the mentioned	L2
12	AT89C2051 has RAM of: A) 128 bytes B) 256 bytes	L1

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	C) 64 bytes D) 512 bytes	
13	8051 series has how many 16 bit registers? A) 2 B) 3 C) 1 D) 0	L1
14	When 8051 wakes up then 0x00 is loaded to which register? A) PSW B) SP C) PC D) None of the mentioned	L1
15	When the microcontroller executes some arithmetic operations, then the flag bits of which register are affected? A) PSW B) SP C) DPTR D) PC	L2
16	How are the status of the carry, auxiliary carry and parity flag affected if the write instruction MOV A,#9C ADD A,#64H A) CY=0,AC=0,P=0 B) CY=1,AC=1,P=0 C) CY=0,AC=1,P=0 D) CY=1,AC=1,P=1	L3
17	How are the bits of the register PSW affected if we select Bank2 of 8051? A) PSW.5=0 and PSW.4=1 B) PSW.2=0 and PSW.3=1 C) PSW.3=1 and PSW.4=1 D) PSW.3=0 and PSW.4=1	L2

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18	<p>If we push data onto the stack then the stack pointer</p> <p>A) increases with every push B) decreases with every push C) increases & decreases with every push D) none of the mentioned</p>	L1
19	<p>On power up, the 8051 uses which RAM locations for register R0- R7</p> <p>A) 00-2F B) 00-07 C) 00-7F D) 00-0F</p>	L1
20	<p>How many bytes of bit addressable memory is present in 8051 based microcontrollers?</p> <p>A) 8 bytes B) 32 bytes C) 16 bytes D) 128 bytes</p>	L2
21	<p>A microcontroller at-least should consist of:</p> <p>A) RAM, ROM, I/O ports and timers B) CPU, RAM, I/O ports and timers C) CPU, RAM, ROM, I/O ports and timers D) CPU, ROM, I/O ports and timers</p>	L1
22	<p>Unlike microprocessors, microcontrollers make use of batteries because they have:</p> <p>A) high power dissipation B) low power consumption C) low voltage consumption D) low current consumption</p>	L1
23	<p>Why microcontrollers are not called general purpose computers?</p> <p>A) because they have built in RAM and ROM B) because they design to perform dedicated task C) because they are cheap D) because they consume low power</p>	L2

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24	<p>How many bytes of bit addressable memory is present in 8051 based microcontrollers?</p> <p>A) 8 bytes B) 32 bytes C) 16 bytes D) 128 bytes</p>	L1
25	<p>What are the two instruction set architecture (ISA) classification</p> <p>A) SIDC & BIDC B) CISC & RISC C) CISC & VISC D) RISC & VISC</p>	L1
26	<p>How many timers / counters 8051 16- bit microprocessor have</p> <p>A) 1 B) 2 C) 5 D) 8</p>	L2
27	<p>Register that is used to holds the memory address of the next instruction to be executed is</p> <p>A) Program Memory B) Program Counter C) Control Unit D) Instruction decoder</p>	L1
28	<p>What do you mean by micro in microcontroller?</p> <p>A) Distance between 2 IC's B) Distance between 2 transistors C) Size of a controller D) Distance between 2 pins</p>	L2
29	<p>What is the bit size of the 8051 microcontroller?</p> <p>A) 8-bit B) 4-bit C) 16-bit D) 32-bit</p>	L1

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30	Name the architecture and the instruction set for microcontroller? A) Van- Neumann Architecture with CISC Instruction Set B) Harvard Architecture with CISC Instruction Set C) Van- Neumann Architecture with RISC Instruction Set D) Harvard Architecture with RISC Instruction Set	L2
31	Number of I/O ports in the 8051 microcontroller? A) 3 ports B) 4 ports C) 5 ports D) 4 ports with last port having 5 pins	L1
32	Is ROM is used for storing data storage? A) True B) False	L2
33	SCON in serial port is used for which operation? A) Transferring data B) Receiving data C) Controlling D) Controlling and transferring	L1
34	Program counter stores what? A) Address of before instruction B) Address of the next instruction C) Data of the before execution to be executed D) Data of the execution instruction	L3
35	Auxiliary carry is set during which condition? A) When carry is generated from D3 to D4 B) When carry is generated from D7 C) When carry is generated from both D3 to D4 and D7 D) When carry is generated at either D3 to D4 or D7	L1
36	The use of Address Latch Enable is to multiplex address and data memory. A) True	L1

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	B) False	
37	<p>Which pin provides a reset option in 8051?</p> <p>A) Pin 1 B) Pin 8 C) Pin 11 D) Pin 9</p>	L2
38	<p>External Access is used to permit _____</p> <p>A) Peripherals B) Power supply C) ALE D) Memory interfacing</p>	L1

